

SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR



www.centralsemi.com

W
A
kV
c
°C
°C

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CTLTVS5V0B is a low capacitance, ultra low leakage, fast response, bi-directional TVS in the space saving TLM2D3D6 surface mount package. This device is designed to protect sensitive equipment connected to high speed data lines against ESD damage.

MARKING CODE: 5





Top View Bottom Vie

TLM2D3D6 CASE

APPLICATIONS:

- High speed data line protection
- User interface protection
- Charging/power port protection
- · Serial/parallel port protection

FEATURES:

- Ultra space saving TLM2D3D6 package
- · Low capacitance
- Ultra low leakage current

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL	
Peak Power Dissipation (8x20µs)	P_{PK}	30
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40
ESD Voltage (IEC 61000-4-2, Air)	V_{ESD}	16
ESD Voltage (IEC 61000-4-2, Contact)	V_{ESD}	16
Operating Junction Temperature	T_J	-55 to +125
Storage Temperature	T _{stg}	-55 to +150

ELECTRICAL CHARACTERISTICS: (T_A=25°C)

Maximum Reverse Stand-off Voltage VRWM	Volt	down tage @ ^I T	Test Current	Maximum Reverse Leakage Current I _R @ V _{RWM}	Clam Volt (8x2	age 0µs)	Typical TLP Clamping Voltage (Note 1) V _{CL} @ I _{PP}		Typical Dynamic Resistance (Note 1) RDYN	Maximum Junction Capacitance @ 0V Bias CJ
V	MIN V	MAX V	mA	μA	V	A	V	A	Ω	pF
5.0		40	4.0	0.4	12	1.0	9.5	4.0	0.5	2.5
5.0	5.5	10	1.0	0.1	15	2.0	11.5	8.0	0.5	3.5

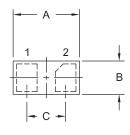
Note 1: Transmission Line Pulse (TLP) conditions: $Z_0=50\Omega$, tp=100ns



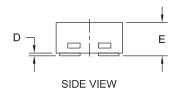
SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR

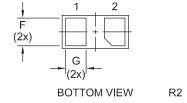


TLM2D3D6 CASE - MECHANICAL OUTLINE



TOP VIEW





DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.022	0.026	0.55	0.65		
В	0.010	0.014	0.25	0.35		
С	0.0	14	0.35			
D	0.000	0.002	0.00	0.05		
E	0.011	0.013	0.28	0.32		
F	0.008	0.012	0.20	0.30		
G	0.005	0.009	0.13	0.24		

TLM2D3D6 (REV: R2)

PIN CONFIGURATION



LEAD CODE:

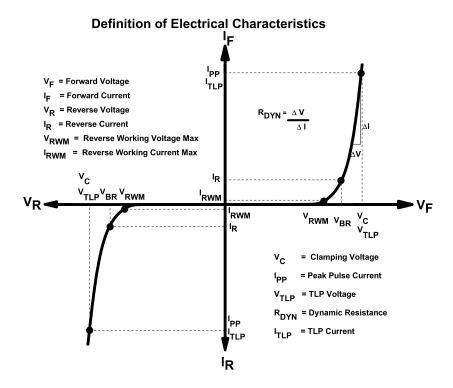
- 1) Anode 1
- 2) Anode 2

MARKING CODE: 5



SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR



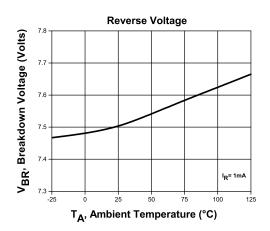


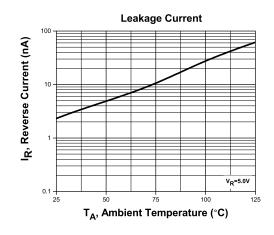


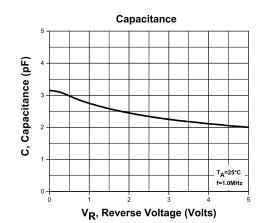
SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR

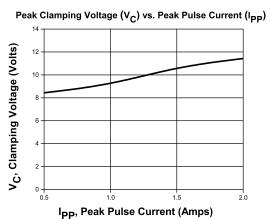


TYPICAL ELECTRICAL CHARACTERISTICS







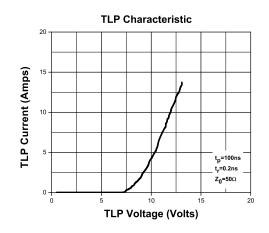


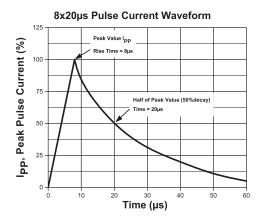


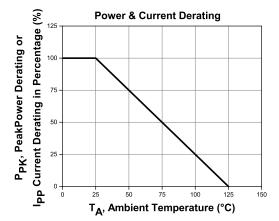
SURFACE MOUNT SILICON BI-DIRECTIONAL LOW CAPACITANCE TRANSIENT VOLTAGE SUPPRESSOR



TYPICAL ELECTRICAL CHARACTERISTICS







OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- · Special wafer diffusions
- · PbSn plating options
- Package details
- · Application notes
- · Application and design sample kits
- · Custom product and package development

CONTACT US

Corporate Headquarters & Customer Support Team

Central Semiconductor Corp. 145 Adams Avenue Hauppauge, NY 11788 USA

Main Tel: (631) 435-1110 Main Fax: (631) 435-1824

Support Team Fax: (631) 435-3388

www.centralsemi.com

Worldwide Field Representatives:

www.centralsemi.com/wwreps

Worldwide Distributors:

www.centralsemi.com/wwdistributors

For the latest version of Central Semiconductor's LIMITATIONS AND DAMAGES DISCLAIMER, which is part of Central's Standard Terms and Conditions of sale, visit: www.centralsemi.com/terms

(000)www.centralsemi.com