Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.51$  V at  $I_F = 10$  A

## **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

## TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

### **MECHANICAL DATA**

#### Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V80H150PW	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	150	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	80	A	
	per diode		40		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	280	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +175	°C	







2 x 40 A

150 V

280 A

0.68 V

175 °C

TO-3PW

Dual common cathode

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

I<sub>FSM</sub>

 $V_F$  at  $I_F = 40$  A

T<sub>J</sub> max.

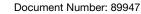
Package

**Diode variation** 



RoHS COMPLIANT HALOGEN

FREE





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ELECTRICAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 10 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.65	-	V	
	I <sub>F</sub> = 20 A			0.74	-		
	I <sub>F</sub> = 40 A			0.82	0.91		
	I <sub>F</sub> = 10 A	T <sub>A</sub> = 125 °C		0.51	-		
	I <sub>F</sub> = 20 A			0.59	-		
	I <sub>F</sub> = 40 A			0.68	0.76		
Reverse current per diode	V <sub>R</sub> = 120 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	2	-	μΑ	
		T <sub>A</sub> = 125 °C		3.1	-	mA	
	V <sub>R</sub> = 150 V	T <sub>A</sub> = 25 °C		-	300	μA	
		T <sub>A</sub> = 125 °C		4.4	48	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}$  Pulse test: Pulse width  $\leq 20\ ms$ 

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V80H150PW	UNIT	
Typical thermal resistance	per diode	$R_{ extsf{ heta}JC}$	0.7	°C/W	
	per device		0.5	C/W	

ORDERING INFORMATION (Example)					
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-3PW	V80H150PW-M3/4W	4.5	4W	30/tube	Tube

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

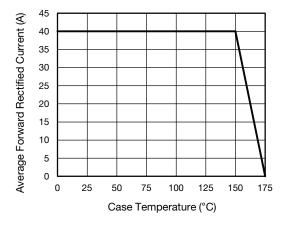


Fig. 1 - Maximum Forward Current Derating Curve

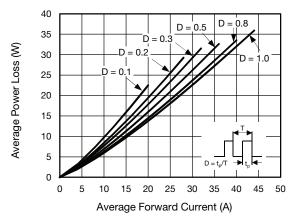
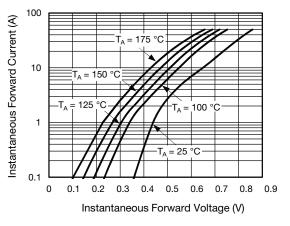


Fig. 2 - Forward Power Loss Characteristics Per Diode

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Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

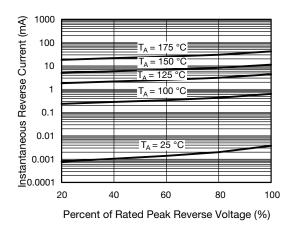


Fig. 4 - Typical Reverse Characteristics Per Diode



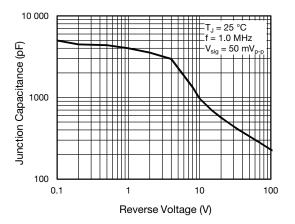


Fig. 5 - Typical Junction Capacitance Per Diode

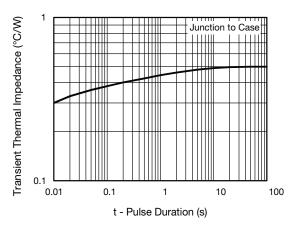
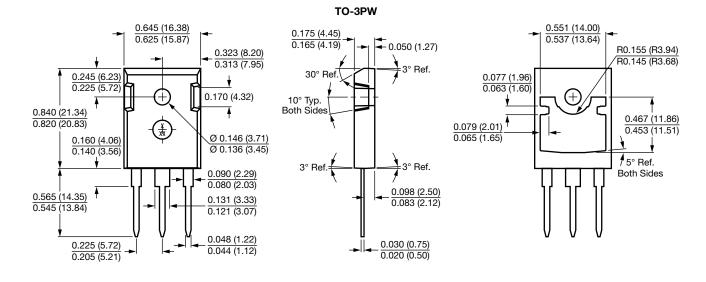


Fig. 6 - Typical Transient Thermal Impedance Per Device



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