TOSHIBA Transistor Silicon-Germanium NPN Epitaxial Planar Type

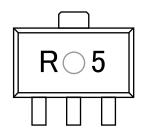
# MT3S111P

VHF-UHF Low-Noise, Low-Distortion Amplifier Applications

#### **Features**

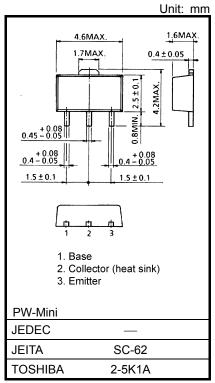
- Low-Noise Figure: NF=0.95 dB (typ.) (@f=1 GHz)
- High Gain:  $|S_{21e}|^2 = 10.5 \text{ dB (typ.)}$  (@f=1 GHz)

### Marking



## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-emitter voltage	V <sub>CES</sub>	13	V
Collector-emitter voltage	V <sub>CEO</sub>	6	V
Emitter-base voltage	V <sub>EBO</sub>	0.6	V
Collector-current	Ic	100	mA
Base-current	ΙΒ	10	mA
Collector power dissipation	PC	300	mW
Collector power dissipation	P <sub>C</sub> (Note 1)	1	W
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C



Weight:0.05 g (typ.)

Note 1: The device is mounted on a ceramic board (16 mm × 16 mm × 0.8 mm (t))

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



## **Microwave Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA	6	8	_	GHz
Insertion gain	S <sub>21e</sub>   <sup>2</sup> (1)	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA, f=500 MHz	_	16	_	dB
	S <sub>21e</sub>   <sup>2</sup> (2)	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA, f=1 GHz	8.5	10.5	_	dB
Noise figure —	NF(1)	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA, f=500 MHz	_	0.7	_	dB
	NF(2)	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA, f=1 GHz	_	0.95	1.25	dB
3 <sup>rd</sup> order intermodulation distortion output intercept point	OIP <sub>3</sub>	$V_{CE}$ =5 V, $I_{C}$ =30 mA, f=500 MHz, $\angle$ f=1 MHz		32		dBmW

## **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> =5 V, I <sub>E</sub> =0 A	_	_	0.1	μΑ
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> =5 V, I <sub>C</sub> =30 mA	200	_	400	_
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =5 V, I <sub>E</sub> =0 A, f=1 MHz	_	1.6	_	pF
Reverse transfer capacitance	C <sub>re</sub>	V <sub>CB</sub> =5 V, I <sub>E</sub> =0 A, f=1 MHz (Note 2)	_	1	1.3	pF

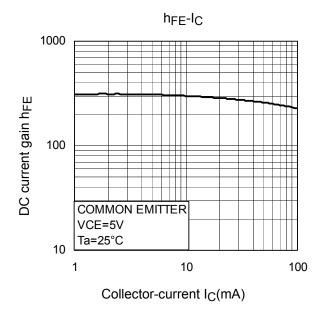
Note 2:  $C_{\mbox{re}}$  is measured using a 3-terminal method with capacitance bridge.

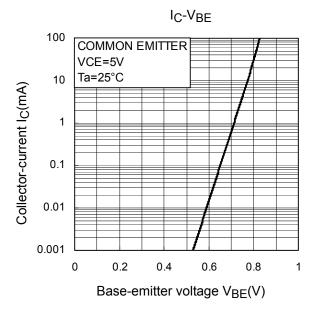
#### Caution:

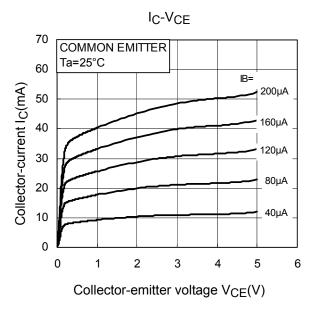
This device is sensitive to electrostatic discharge due to the high frequency transistor process of

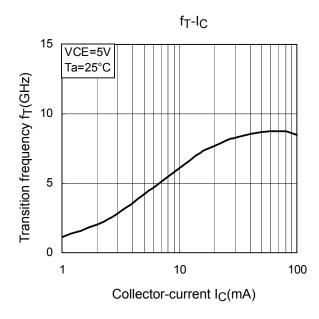
 $f_T$ =60 GHz class which is used for this product.

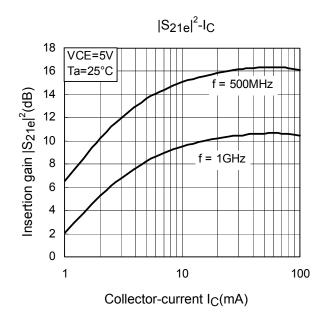
Please make tool and equipment earthed enough when you handle.

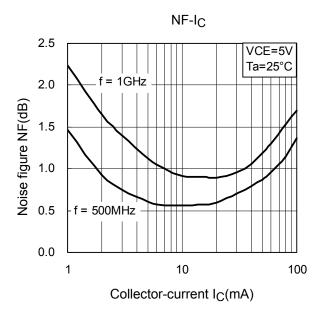


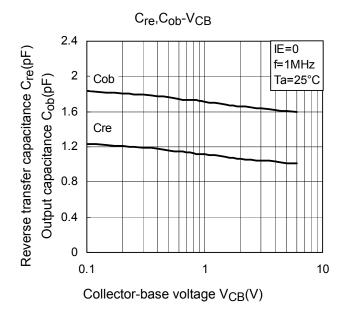


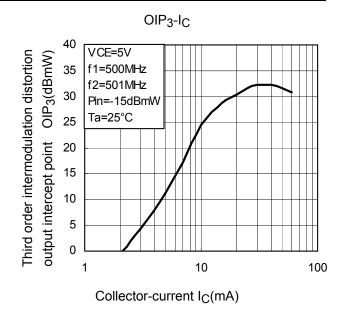


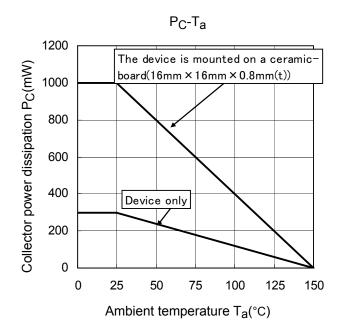












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